



## Tellabs 5500NGX DS3 Test Board TB-5500NGXDS3 Practice

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### **GENERAL**

This practice describes Telecom Assistance Group's TB-5500NGXDS3 - Tellabs 5500NGX DS3 Test Board. The TB-5500NGXDS3 is used when installing the Tellabs 5500NGX DS3 system.

The TB-5500NGXDS3 is a temporary test card that plugs into the 5500NGX shelf. The Test Board is used to verify wiring and perform DS3 signal acceptance to the DSX-3/4. Acceptance testing determines whether or not the card slots are properly connected through the interconnect wiring and able to Transmit and Receive a good DS3 signal.

### **APPLICATION**

The TAG 5500NGX DS3 Test Board (TB-5500NGXDS3) is utilized by installers and audit (acceptance) personnel. The Test Board has 440A jack access allowing metallic wiring verification and DS3 signal acceptance to the DSX-3/4 using 2 standard DS3 test sets (not included). The "acceptance" test determines whether or not the card slots are properly connected through the interconnect wiring and able to Transmit and Receive a good DS3 signal.

Note: The TB-5500NGXDS3 Test Board is a temporary test board. Do not leave Test Board unattended while plugged into a Card Slot.

### **PROCEDURE**

Acceptance Testing requires two (2) people, and 2 DS3 test sets. Tester 1 will be located at the 5500NGX DS3 with the TB-5500NGXDS3, and Tester 2 will be at the DSX-3.

Tester 1 and Tester 2 will alternate between transmitting and receiving DS3 signals to perform acceptance testing.

See Pages 3 & 4 for Step-by-Step Test Procedures.

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### 4 Physical Description/Wiring

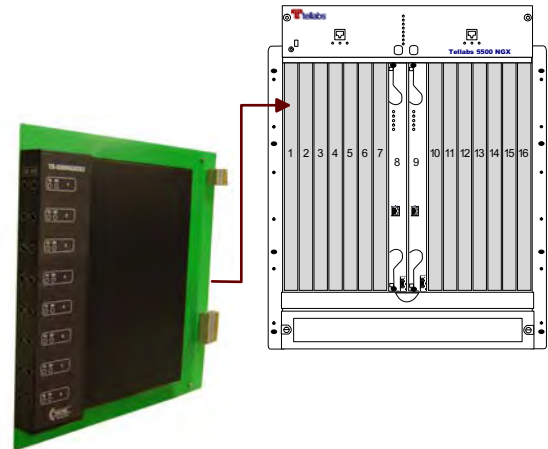
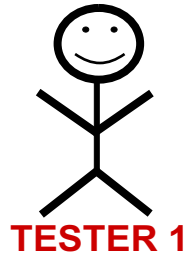


The TB-5500NGXDS3 has 440A jack access allowing metallic wiring verification and DS3 signal acceptance testing to the DSX-3/4. Tester 1 will transmit a DS3 test signal to the DSX-3/4. At the DSX-3/4, Tester 2 will verify "good" DS3 signal being received on DSX-3/4 - CKT 1 OUT jack. Tester 2 will then transmit from DSX-3/4 CKT 1 IN jack to Tester 1 at 5500NGX Slot 1 - RX1. Tester 1 will record that a "Good" DS3 signal has been transmitted and received. The wiring label on TB-5500NGXDS3 indicates 440A Jacks for access to the 8 DS3 circuits.

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### 5 Step by Step Test Procedure

**When plugging the Test Board into the 5500NGX DS3 shelf use normal static procedures.**



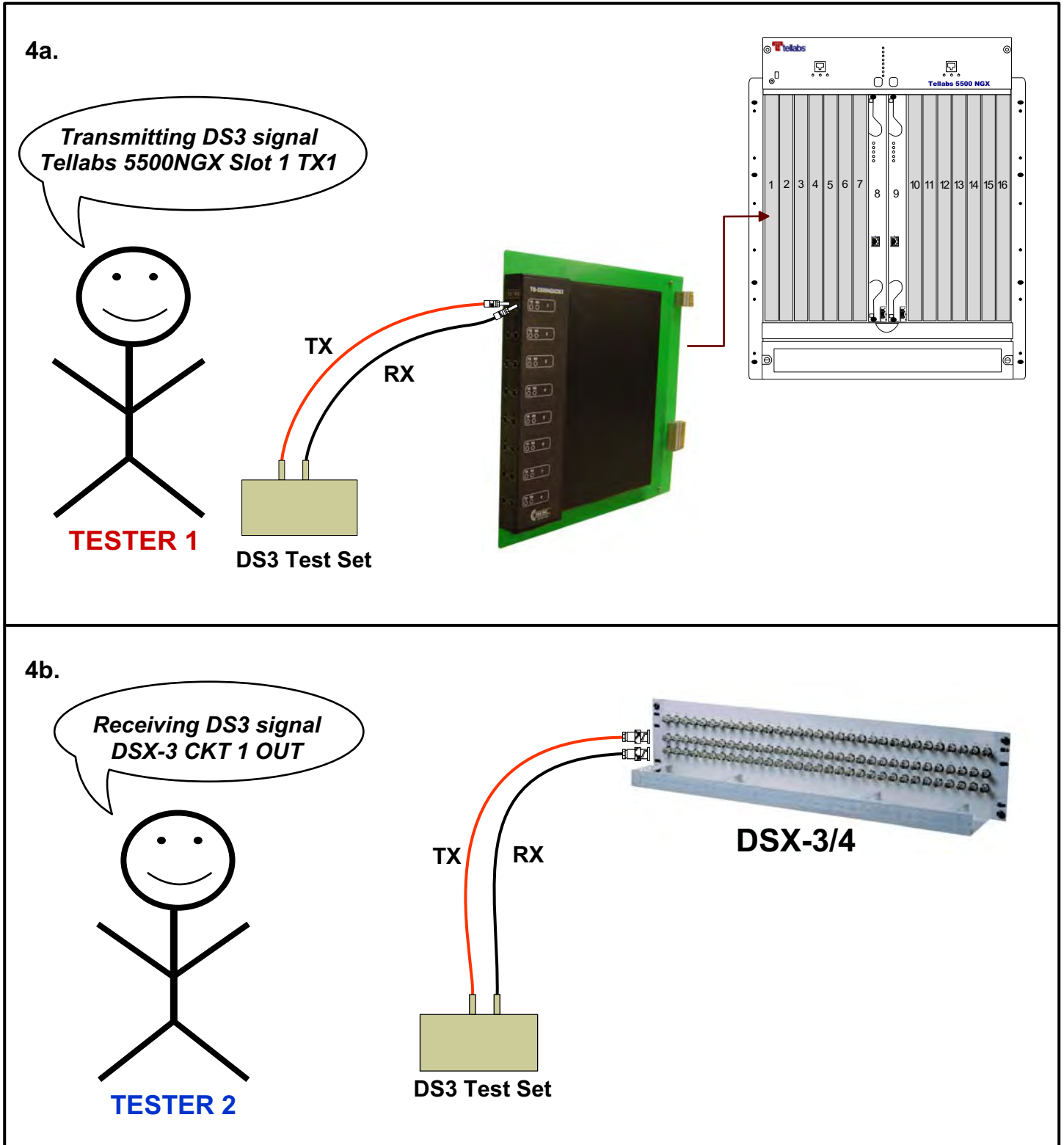
Step	Procedure
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1. Visually check connector on Test Board and backplane of 5500 NGX card slot for connector wear or pin obstruction.
2. Verify proper alignment and insert TB-5500NGXDS3 into card slot .
3. Establish communication with Tester 2 at the DSX-3/4.
- 4a. Tester 1 connects a DS3 test set to circuit 1 on the TB-5500NGXDS3. (see page 4)
- 4b. Tester 2 connects a DS3 test set to the circuit transmitting points on a DSX-3/4 panel. (see page 4)
5. Tester 1 will transmit a DS3 test signal from 5500 NGX Slot 1 TX1.
6. Tester 2 will record that a "Good" DS3 signal has been received at DSX-3/4 CKT 1 OUT.
7. Tester 2 will transmit a DS3 test signal from DSX-3/4 IN.
8. Tester 1 will record that a "Good" DS3 signal has been received at 5500 NGX Slot 1 RX1.
9. The Testers will move to the next circuit and perform the same test until all the circuits have been tested and approved.
10. Record data in Test Acceptance Data Sheet - See Page 5.



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### 5 Step by Step Procedure Continued





## Tellabs 5500NGX DS3 Test Board TB-5500NGXDS3 Practice

Tested by : \_\_\_\_\_ Shelf # : \_\_\_\_\_  
 Rack # : \_\_\_\_\_

### 6 Test Acceptance Data Sheet

(P) Pass (F) Fail

Tellabs 5500NGX DS3			
Slot __ - 5500NGX	Tellabs 5500NGX	DSX-3/4	DS3 Test Result
<b>Circuit 1</b>	TX 1	OUT	
	RX 1	IN	
<b>Circuit 2</b>	TX 2	OUT	
	RX 2	IN	
<b>Circuit 3</b>	TX 3	OUT	
	RX 3	IN	
<b>Circuit 4</b>	TX 4	OUT	
	RX 4	IN	
<b>Circuit 5</b>	TX 5	OUT	
	RX 5	IN	
<b>Circuit 6</b>	TX 6	OUT	
	RX 6	IN	
<b>Circuit 7</b>	TX 7	OUT	
	RX 7	IN	
<b>Circuit 8</b>	TX 8	OUT	
	RX 8	IN	
			Pass (P) Fail (F)