



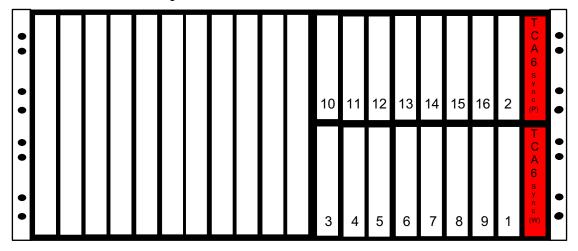
The STB-FUJ4100BITS provides a quick method to verify continuity, as well as, transmit a test signal from the FLASHWAVE 4100 BITS card slots to the DSX-1. At the DSX-1, Tester 2 will use a continuity test probe to verify that the BITS card slot is properly wired. Tester 2 is also capable of looping a test signal back to the STB-FUJ4100BITS and Tester 1 can determine if the signal is acceptable for system turn up. The STB-FUJ4100BITS is used on non-powered systems.



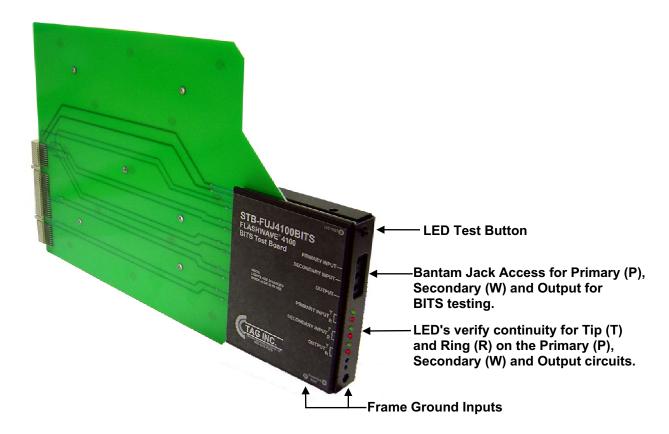
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Physical Description and Wiring

Fujitsu FLASHWAVE* 4100 Shelf

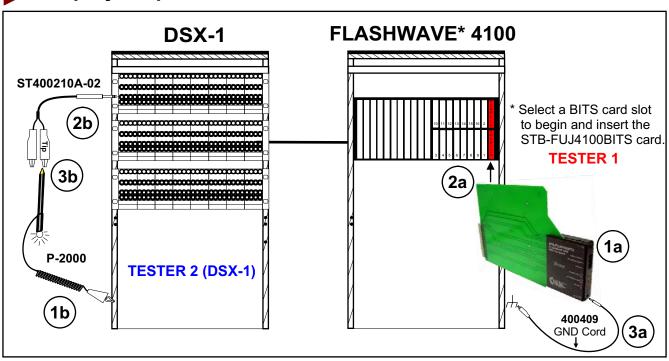


*The (STB-FUJ4100BITS) FLASHWAVE 4100 BITS Streaker/Test Board will plug into slots TCA6 (P) & (W) color coded red.





Step by Step Procedure



TESTER 1 (Fujitsu FLASHWAVE 4100 BITS Continuity Test)

- 1a. Press LED test button. Verify that all LED's illuminate. If LED's do not illuminate, replace with a new battery.
- 2a. Select BITS card slot TCA6 (P) or (W) to begin testing.

 (CAUTION: Do not force. Verify proper alignment before inserting.)
- 3a. If chassis ground is not already connected through the backplane, insert Pin plug test cord (# 400409) into the STB-FUJ4100BITS Card and connect the Alligator Clip to Frame Ground.
- 4a. Establish communication with Tester 2 at the DSX-1. You are ready to begin testing at the Fujitsu FLASHWAVE 4100 (observe LED's illuminating).

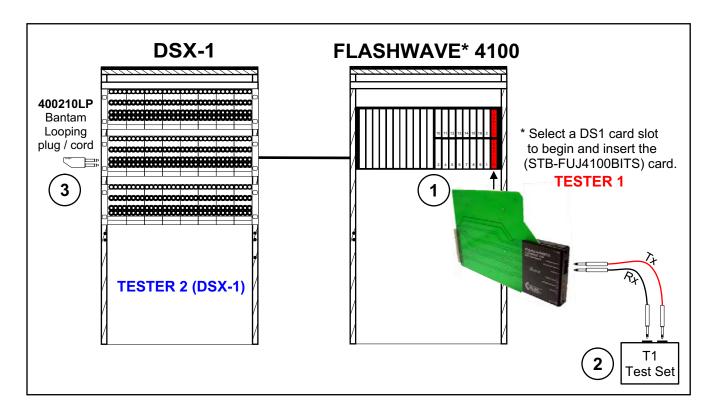
TESTER 2 (DSX-1)

- 1b. Connect test probe (P-2000) Alligator Clip to Frame Ground.
- 2b. Plug Bantam to Alligator Clips cord in DSX-1 jack to begin testing
- 3b. Touch Probe end to "ground the corresponding wiring assignments." The LED on the probe will illuminate to indicate a connection to the STB-FUJ4100BITS Card.

LED (Primary Input Tip) = DSX-1 (Primary Input Tip)



► Step by Step Procedure



Primary and Secondary BITS Test Acceptance Procedure:

- Select BITS card slot TCA6 (P) or (W) to begin testing.
 (CAUTION: Do not force. Verify proper alignment before inserting.)
- 2. Using a T1 Test Set, connect bantam cords (400210RD)-Transmit (Tx) and (400210BK)-Receive (Rx) to the appropriate jacks from the STB-FUJ4100BITS to a T1 test set to perform acceptance testing.
- Establish communication with Tester 2 at the DSX-1. Determine which circuits you will be testing. Tester 2 will loop the test signal back to Tester 1 with a bantam looping plug/cord (400210LP).
- 4. Tester 1 will verify that the signal transmitted is acceptable for qualification purposes.